

1/16

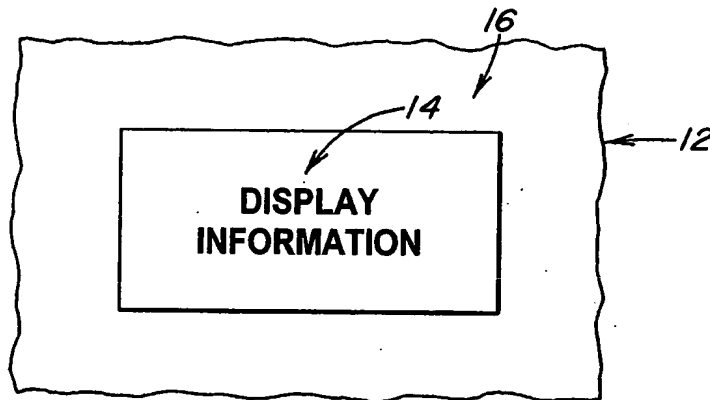


Fig. 1

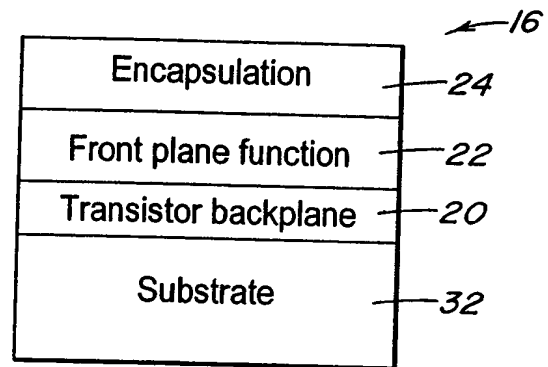


Fig. 2a

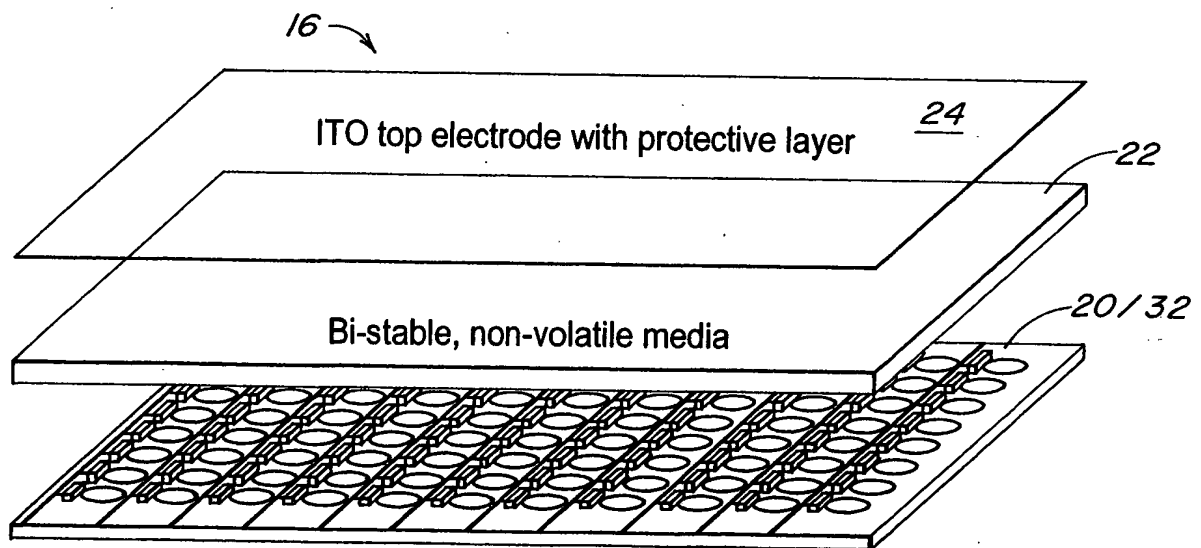


Fig. 2b

2 / 16

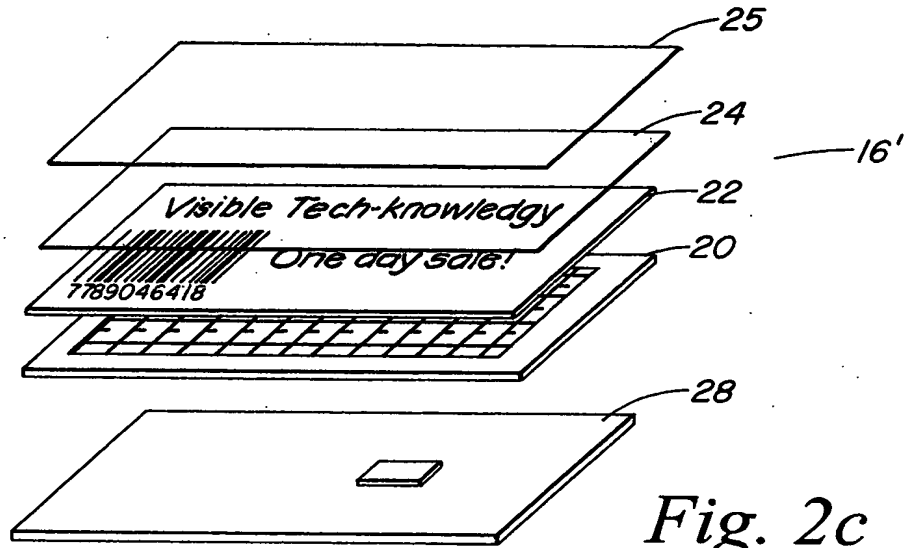


Fig. 2c

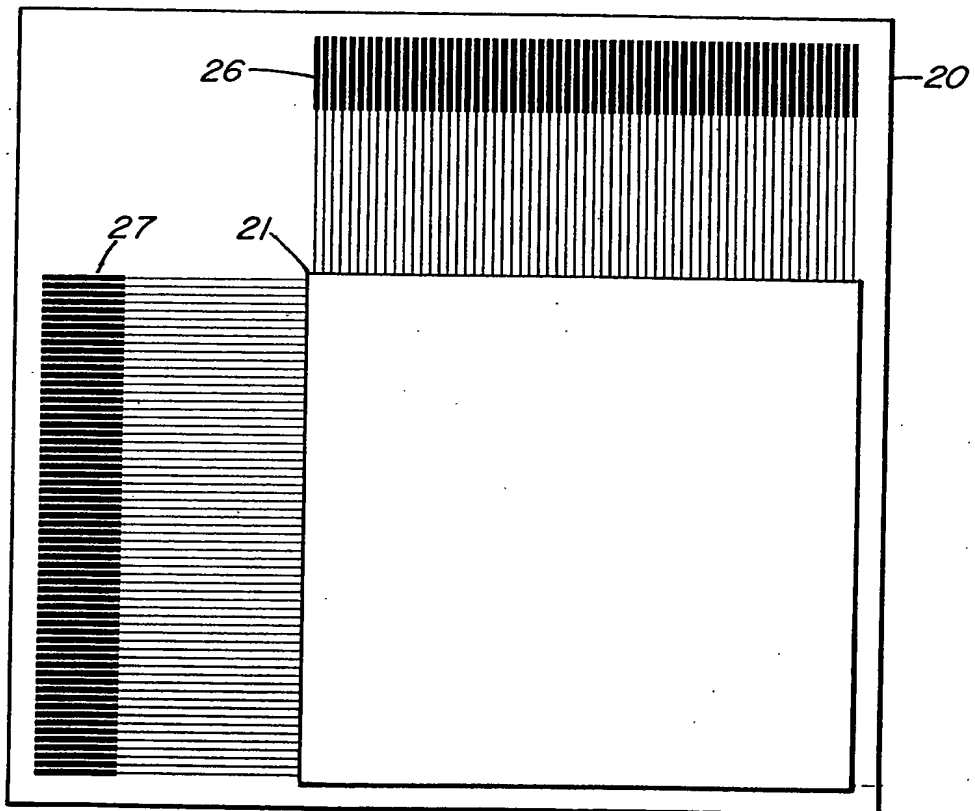


Fig. 3

3/16

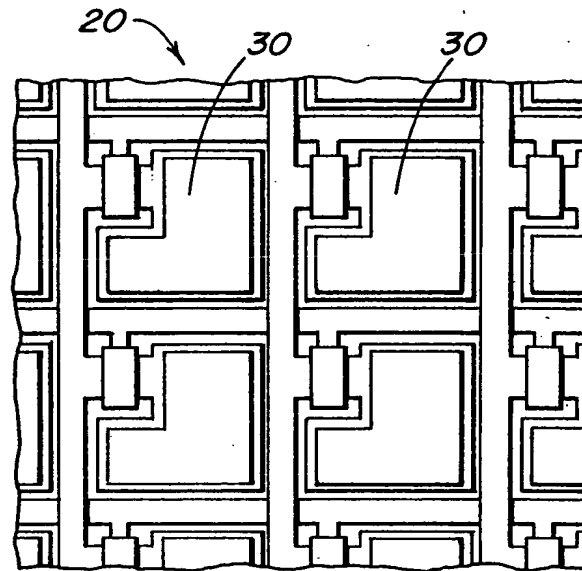


Fig. 4

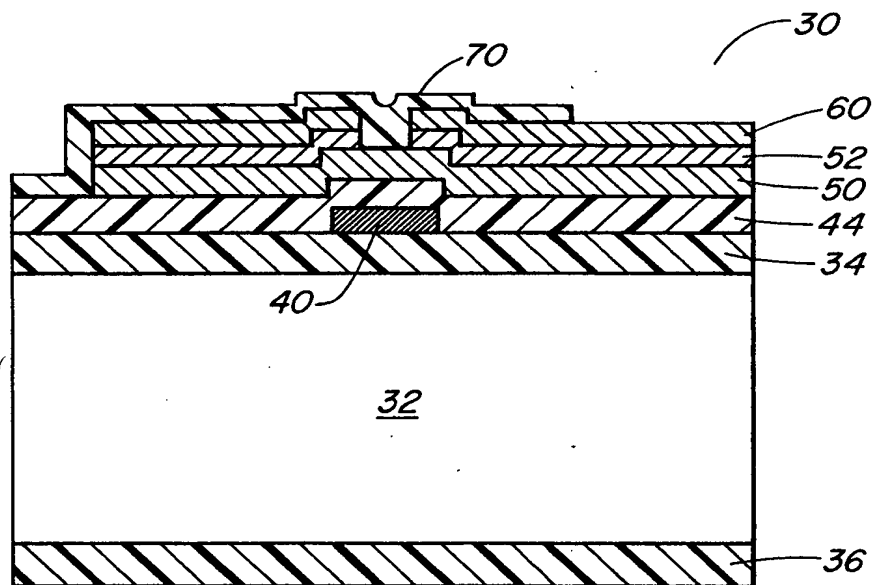


Fig. 5

4/16

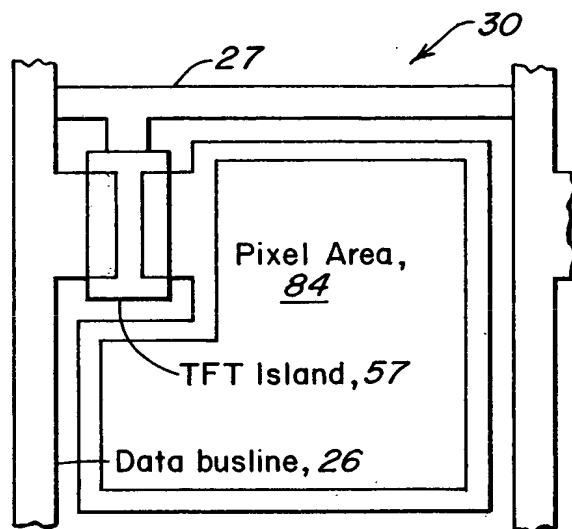


Fig. 6

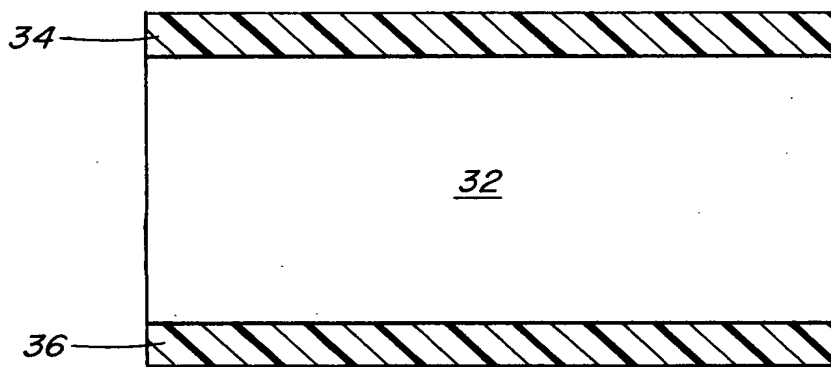


Fig. 7

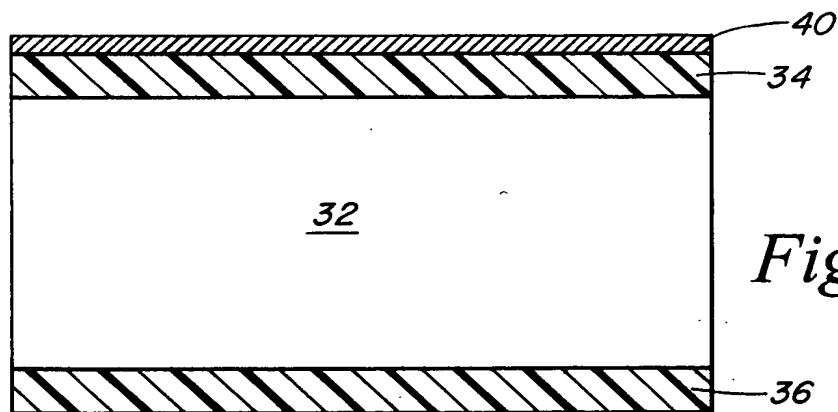


Fig. 8

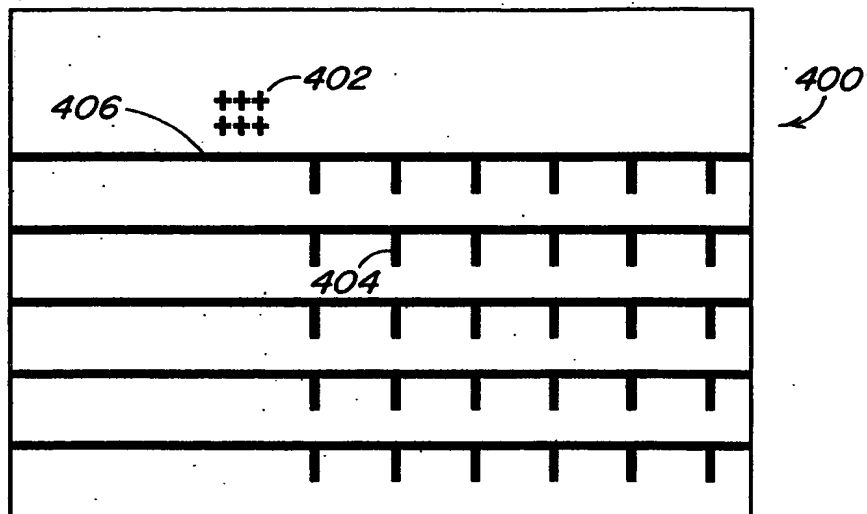


Fig. 9

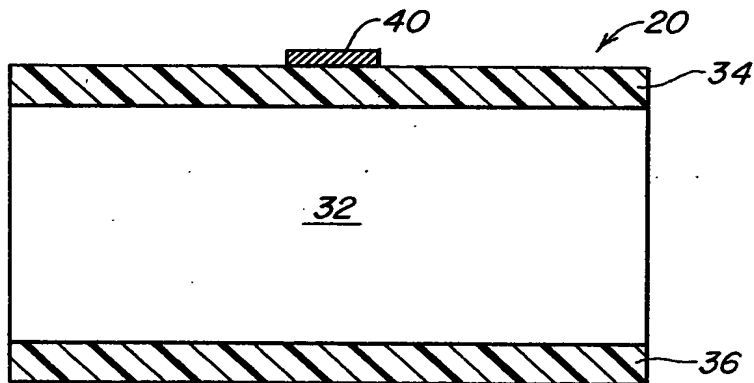


Fig. 10a

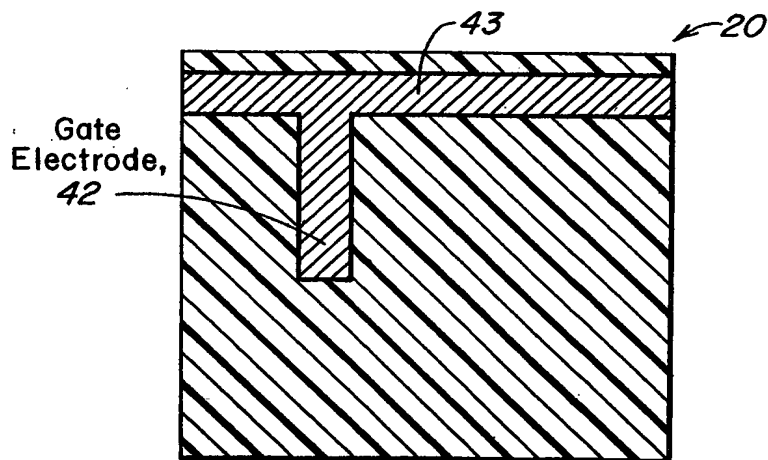


Fig. 10b

6/16

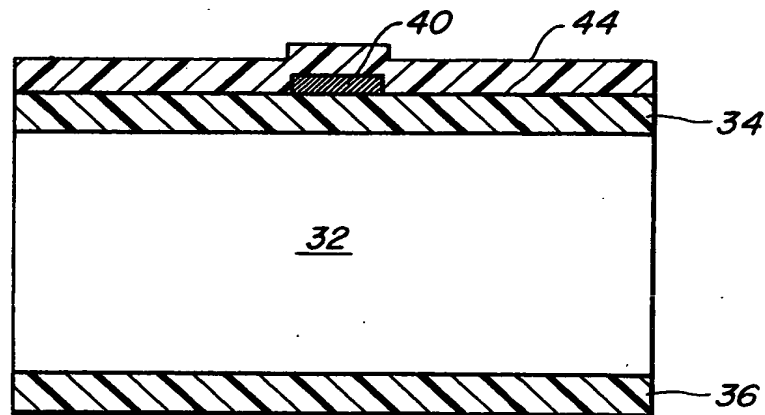


Fig. 11

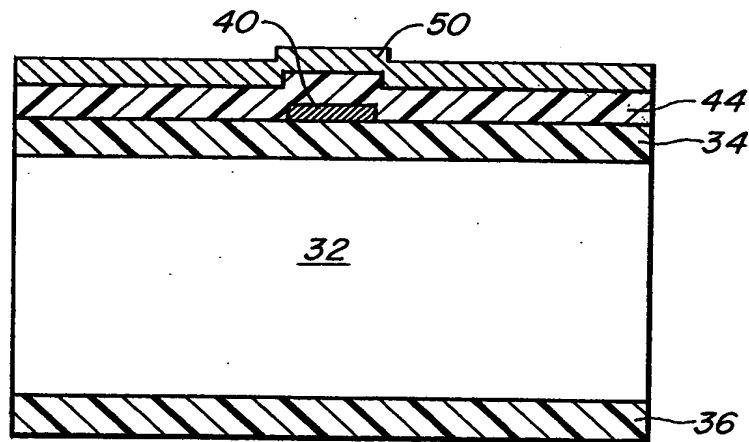


Fig. 12

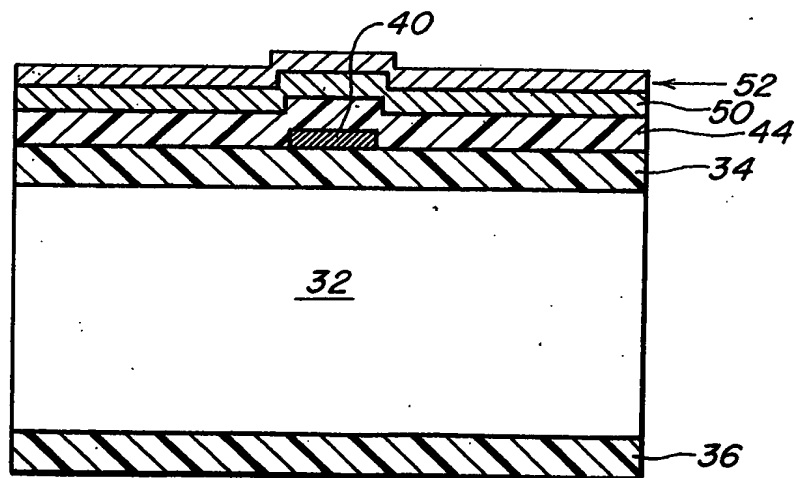


Fig. 13

7/16

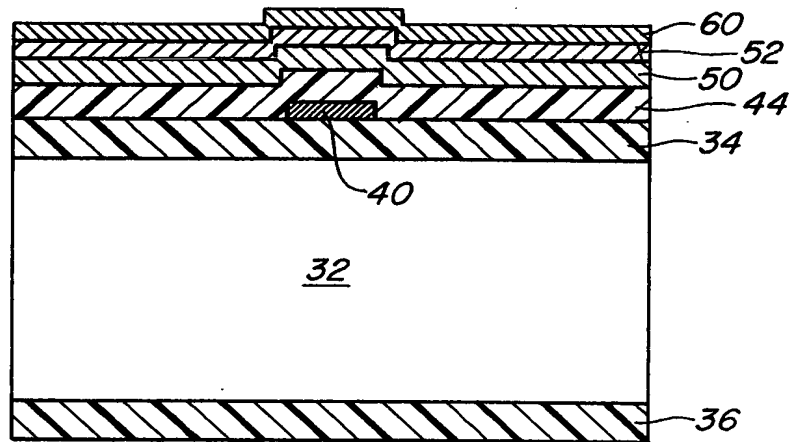


Fig. 14

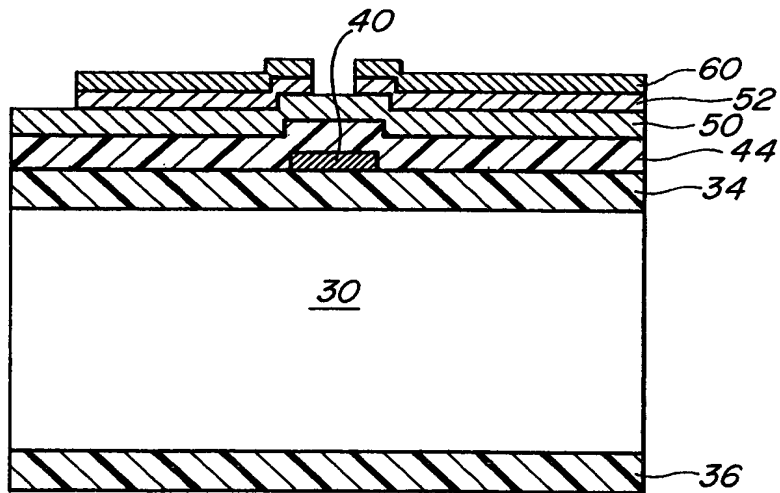


Fig. 15a

8/16

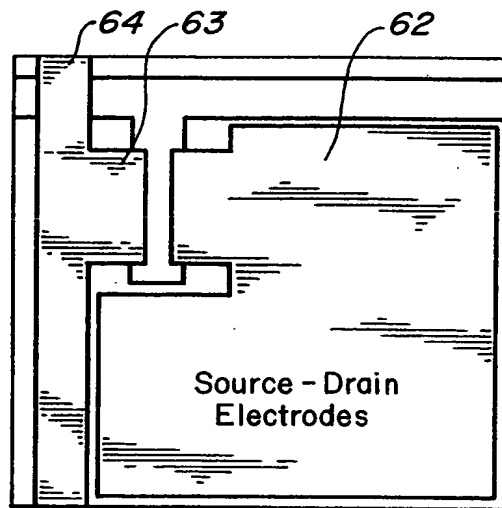


Fig. 15b

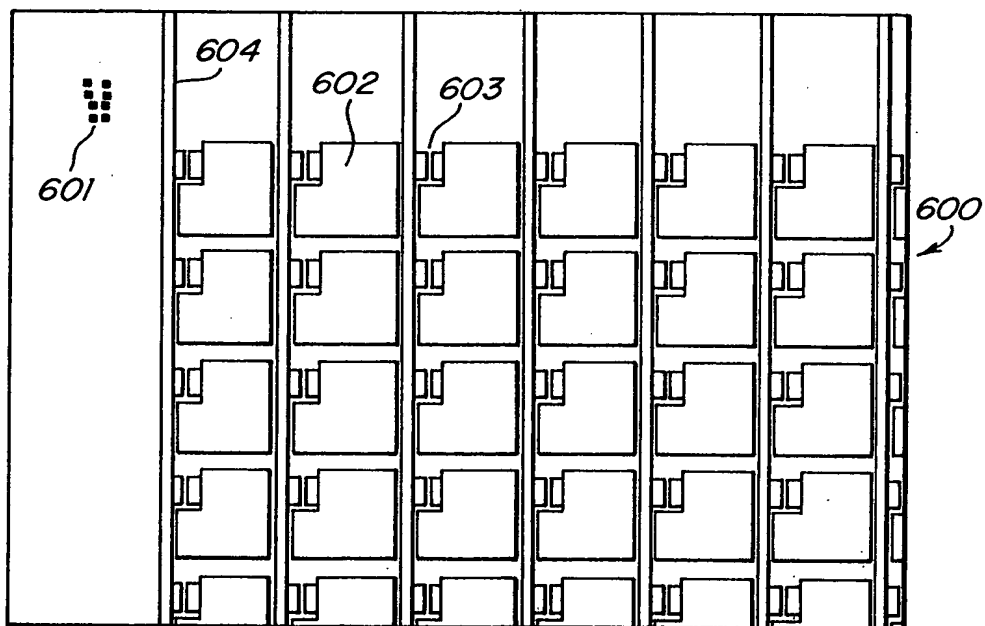


Fig. 16

9/16

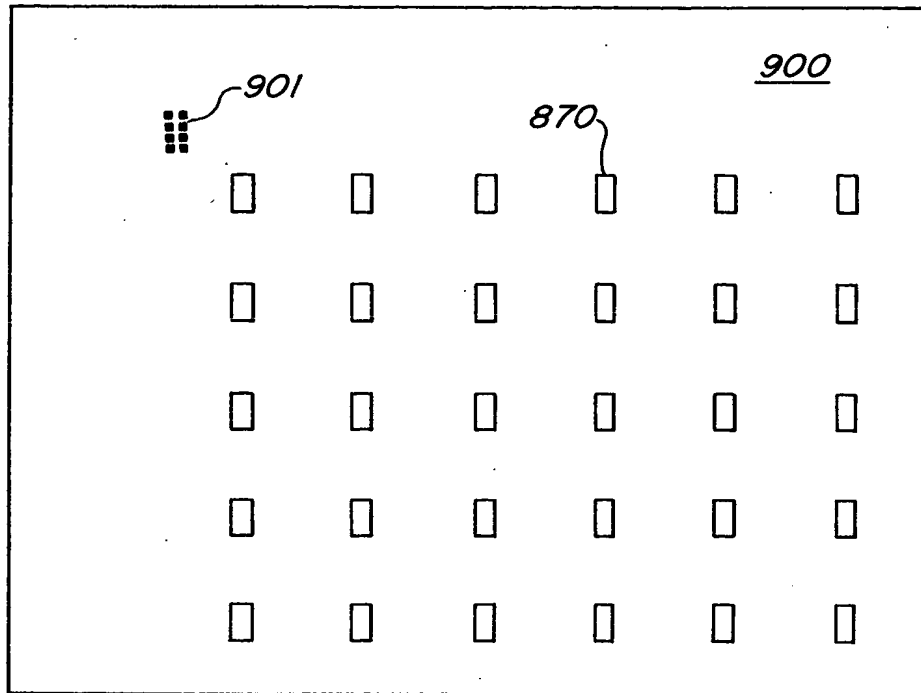


Fig. 17

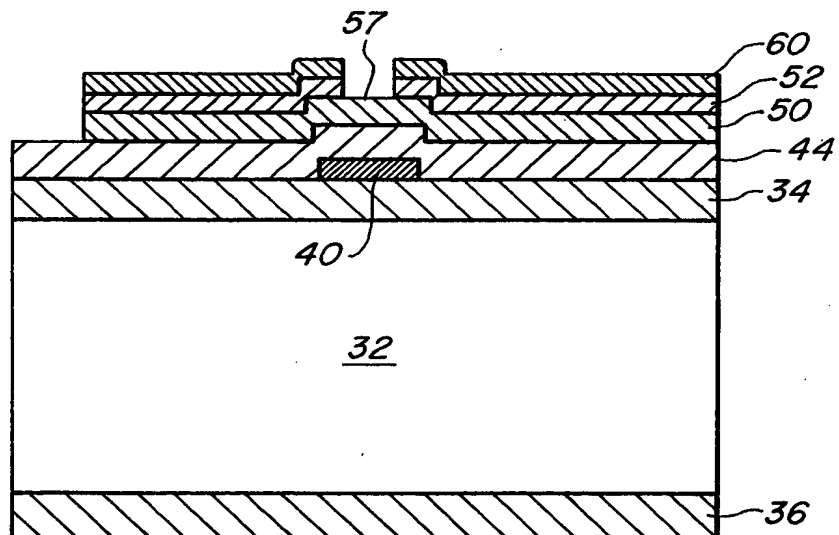


Fig. 18a

10/16

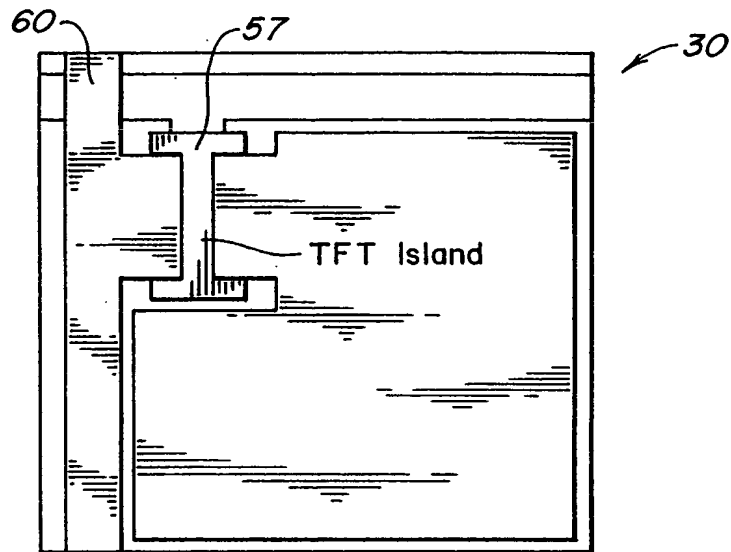


Fig. 18b

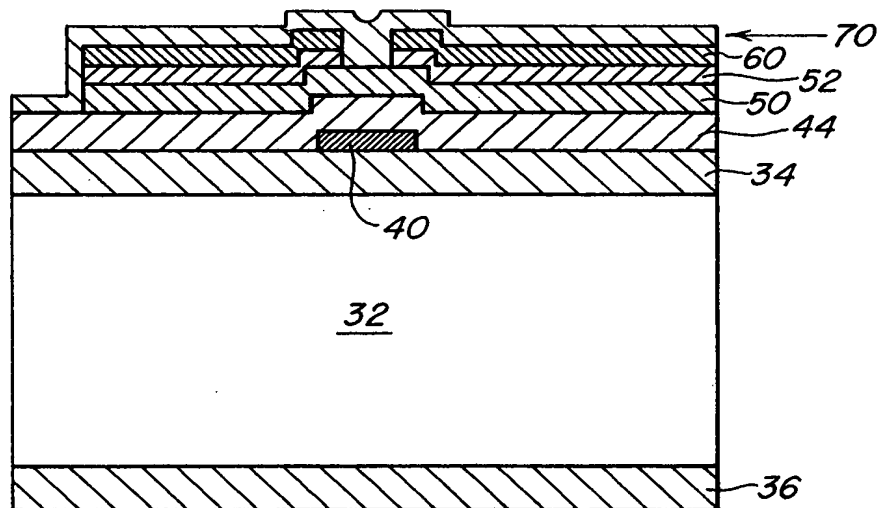
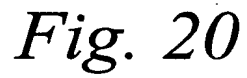


Fig. 19



App No.: Filed Herewith
Inventor: Forbes, et al.
Docket No.: VTW-010DV2
Title: Active Matrix Thin Film Transistor Array...

12/16

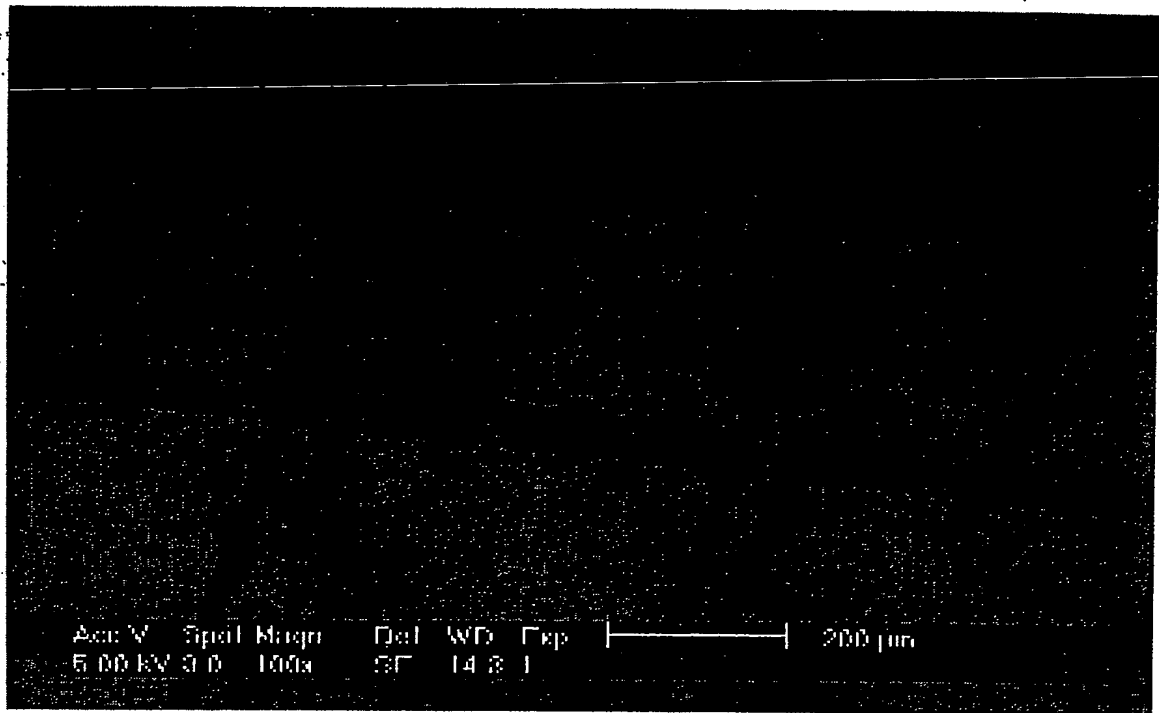


Fig. 22

13/16

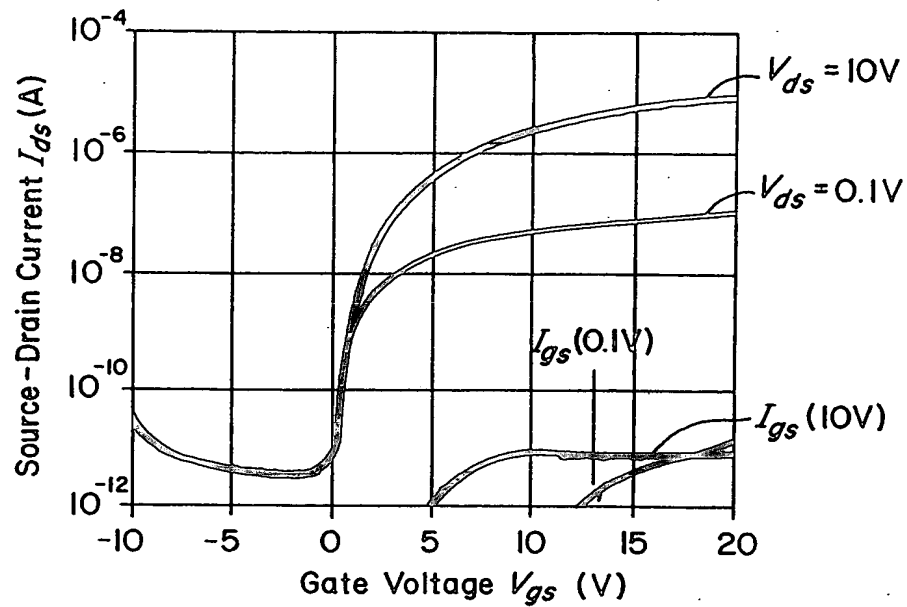


Fig. 23

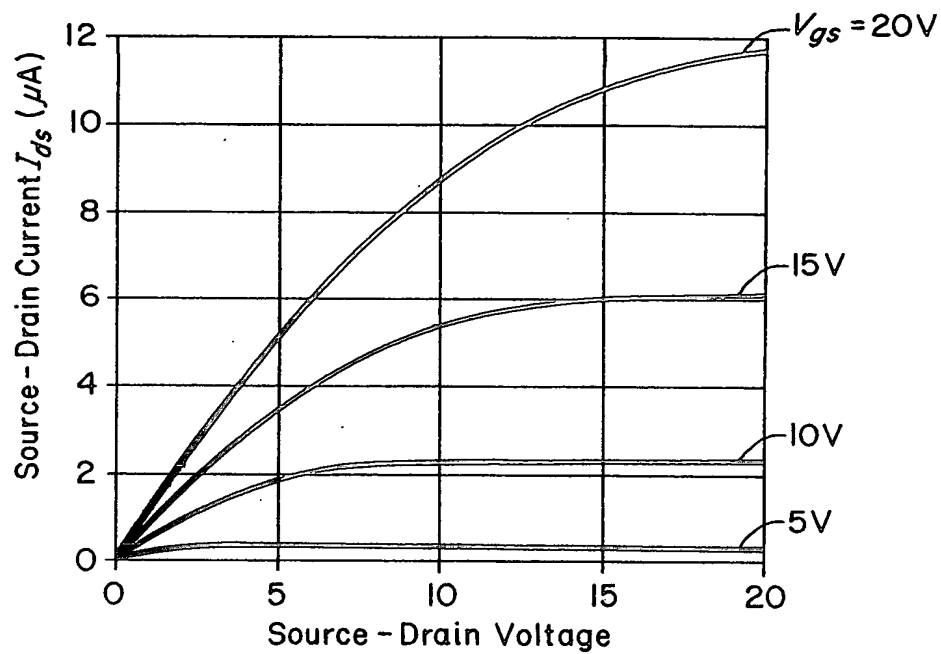


Fig. 24

14/16

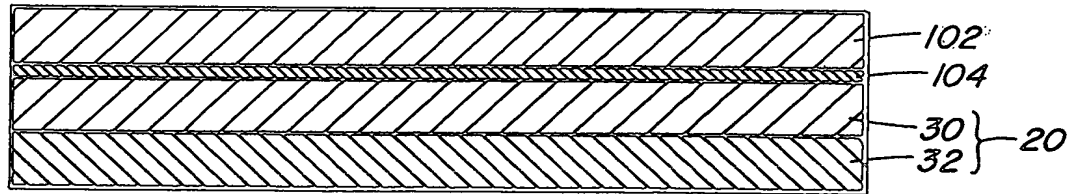


Fig. 25

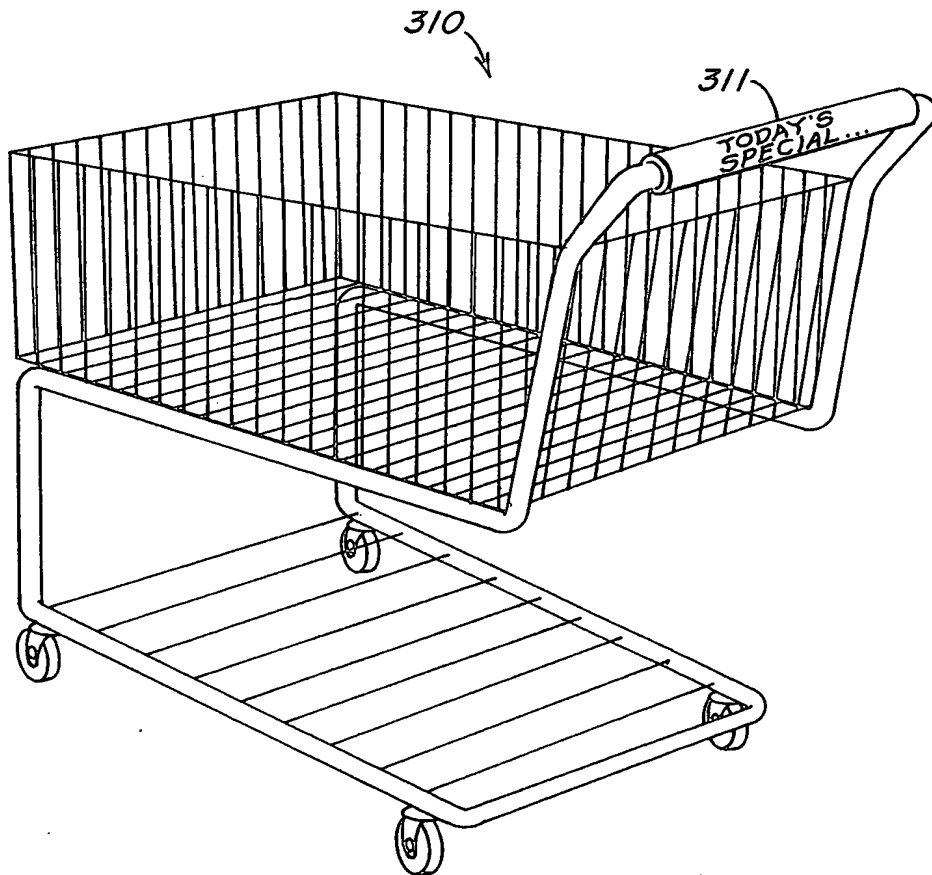


Fig. 26

15 / 16

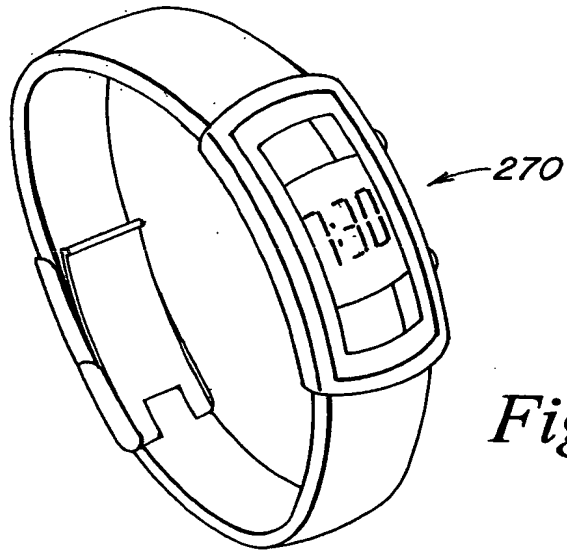


Fig. 27

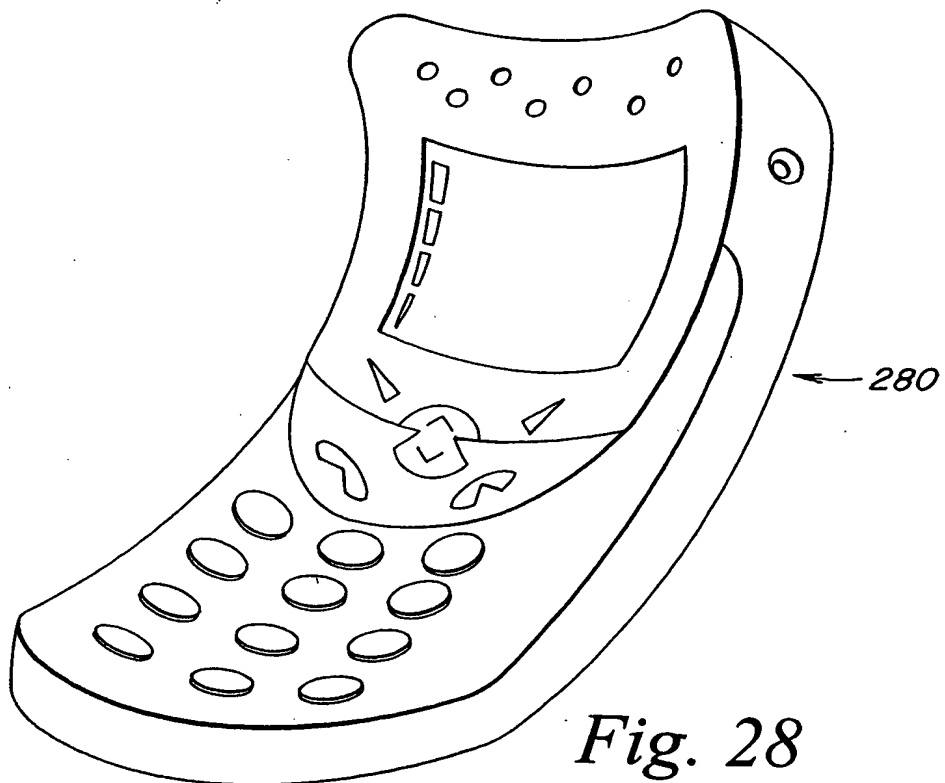


Fig. 28

16/16

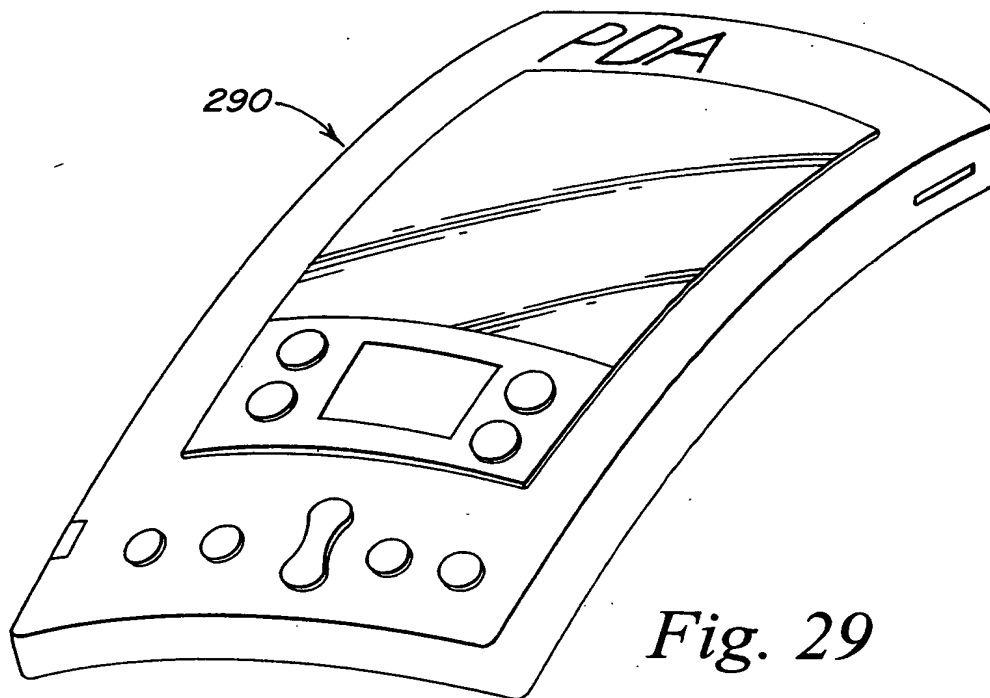


Fig. 29

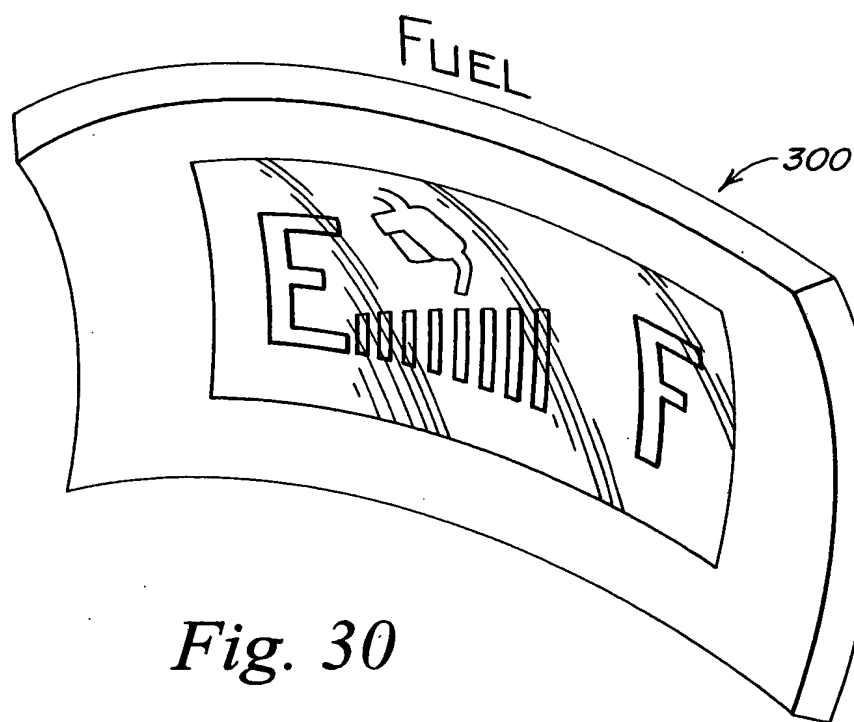


Fig. 30